

#10 IDS w/ ref
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PATENT

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Applicant : Jafar Savoj
Application No. : 09/782,687
Filed : February 12, 2001
Title : LINEAR HALF RATE PHASE DETECTOR AND CLOCK AND DATA
RECOVERY CIRCUIT

Grp./Div. : 2633
Examiner : N. Curs

Docket No. : 50991/DBP/B600

INFORMATION DISCLOSURE STATEMENT WITH FEE
UNDER 37 CFR §§ 1.97(c) AND 1.17(p)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PostOffice Box 7068
Pasadena, CA 91109-7068
March 5, 2004

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO/SB/08A/B listing a reference that is known to applicant. A copy of the listed reference is enclosed.

It is respectfully requested that the listed reference be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO/SB/08A/B be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

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Application No. 09/782,687

Enclosed is the processing fee of \$180 as required by 37 CFR § 1.17(p). The Commissioner is hereby authorized to charge any fees which may be required by this paper to Deposit Account No. 03-1728. Please show our docket number with any Deposit Account transaction. **A copy of this paper is enclosed.**

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By 
Stephen D. Burbach
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626/795-9900

SDB/sdb

Enclosures: Check \$180
Copy of IDS
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